Amendments to the Specification:

Please replace paragraph **[0005]**, on page 1, with the following amended paragraph:

This application is related to the following commonly assigned, copending application, serial number ~~—————— filed, ——————~~, 09/851,934 filed May, 10, 2001, and entitled: ALTERNATIVELY ACCESSED PARALLEL LOOKUP TABLES FOR LOCATING INFORMATION IN A PACKET SWITCHED NETWORK ~~(attorney docket no. F0674)~~.

Please replace paragraph **[0018]**, on page 3, with the following amended paragraph:

A second aspect of the present invention relates to a method of using a lookup table implemented with a first lookup sub-table and a second lookup sub-table. The method comprises: calculating a row address of the lookup table based on a hash value of a network address associated with an entry in the lookup table; determining whether the information is to be stored in the first or the second sub-table based on a pre-selected bit in the information; and <u>accessing the entries stored in the lookup table by reading entries stored at a desired address in the first and second sub-tables.</u>

Please delete paragraph **[0019]**, on page 3, in its entirety.

Please replace paragraph **[0082]**, on page 17, with the following amended

paragraph:

If a bin such as bin 1, 800b, contains a single table entry, the bin entry will store the switching logic data for that single address in its address entry field, and store the value "zero" in the next pointer field, indicating there are no further address entries in the chain. Bin 0, 800a ~~100a~~, however, references three addresses 810a – 810c by using the next pointer field to identify the location of the next entry in the chain. Bins 810b and 810c are linked in a linear list, as shown in Figure 8. Thus, the first entry of bin 0 is stored in the address entry field of the bin entry 800a and the next entry (heap entry 810a) is referenced by address entry "a" in the next pointer field of the bin entry 800a.